PACE 2111 * RCVD AT 411512005 6:00:28 AM [Eastern Daylight Time] * SVR:USPTO-EFXRF-110 * DNIS:8729306 * CSID:87719757 * DURATION (mm-ss):03-26

Applicant: Adlink Technology Inc.

Application No.: 09/994,726

Examiner: Truong, Lechi

Art Unit: 2126

IN THE CLAIMS

Please amend the claims as follows.

1. (previously presented) A control interface card connected to the CPU (central

processing unit) of a host computer, adapted for auto-reloading object position data,

comparing object position data, and providing a triggering signal to the CPU of the host

computer, said control interface card comprising a data buffer adapted for registering a

plurality of object position data computed by the CPU of said host computer, a position

compare circuit adapted for fetching a registered object position data from said data

buffer and comparing the fetched object position data with a feedback position data

obtained via an object shifting control means, and then fetching a next registered object

position data from said data buffer and comparing the next registered object position

data with a feedback position data obtained via the object shifting control means, and a

trigger I/O circuit adapted for providing a triggering signal to the CPU of said host

computer when one of the registered position data is determined to match with one of

the feedback position data by said position compare circuit.

2. (original) The control interface card as claimed in claim 1, which is a motion

control interface card.

3. (original) The control interface card as claimed in claim 1, which is an

industrial counting interface card.

4. (original) The control interface card as claimed in claim 1 further comprising

a bus controller connected to a bus at the CPU of said host computer for

intercommunication between the control interface card and the CPU of said host

computer, and a bus arbitrator connected to said bus controller and adapted for

providing an interrupt signal to the CPU of said host computer through said bus

2

PAGE 3/11 * RCVD AT 41/5/2005 6:00:28 AM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/10 * DNIS-1879306 * CSID:5776-1777 * DURATION (mm-ss):03-26

Applicant: Adlink Technology Inc.

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controller upon matching of one comparison at said position compare circuit.

5. (presently presented) A control interface card connected to the CPU (central

processing unit) of a host computer, comprising:

a data buffer, for registering a plurality of object position data computed by the

CPU of said host computer;

a comparing means, for sequentially fetching a registered object position data

from said data buffer and comparing with a feedback position data obtained via an

object shifting control means; and

a trigger I/O circuit, for providing a triggering signal to the CPU of said host

computer when one of the registered position data is determined to match with one of

the feedback position data by said position compare circuit.

6. (previously presented) The control interface card as claimed in claim 5, which

is a motion control interface card.

7. (previously presented) The control interface card as claimed in claim 5, which

is an industrial counting interface card.

8, (previously presented) The control interface card as claimed in claim 5 further

comprising a bus controller connected to a bus at the CPU of said host computer for

intercommunication between the control interface card and the CPU of said host

computer, and a bus arbitrator connected to said bus controller and adapted for

providing an interrupt signal to the CPU of said host computer through said bus

controller upon matching of one comparison at said position compare circuit.

3